

Electronic Acknowledgement Receipt

EFS ID:	2586684
Application Number:	10716146
International Application Number:	
Confirmation Number:	5288
Title of Invention:	Time delay compensation circuit comprising delay cells having various unit time delays
First Named Inventor/Applicant Name:	Geun-Hee Cho
Customer Number:	22150
Filer:	Frank Chau
Filer Authorized By:	
Attorney Docket Number:	8021-180 (SS-18400-US)
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	8021-180-amd2.pdf	94712	no	5
			3c519ed008cda2e1658cbe1854c3d851d8abde144		

Warnings:**Information:**

2	Fee Worksheet (PTO-06)	fee-info.pdf	8170	no	2
			8100055e6f483653420980bde58b0eabdc5d21fa32		

Warnings:**Information:**

Total Files Size (in bytes):	102882
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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New International Application Filed with the USPTO as a Receiving Office

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